A

Please type a plus sign (+) inside this box → +		Pater	at and	Approved for	use thro	PTO/SE ough 09/30/2000. OMB S. DEPARTMENT OF C	3/05 (4/98) 0651-0032
Under the Paperwork Reduction Act of 1995, no persons are required		a collection	on of i	nformation uni	less it dis	splays a valid OMB cont	ro! number
UTILITY	Attorney D	ocket No	p. 10	03.1032.02	2		P.T
PATENT APPLICATION	First Inven	tor or Ap	plicat	ion Identifier	Eng	lish, Robert M.	40
TRANSMITTAL				High-Concu			u. 90
(Only for new nonprovisional applications under 37 C.F.R. § 1.53(b))	Express M	ail Label	No.	EL 524 7	81 248	3 US	14
			_				
APPLICATION ELEMENTS See MPEP chapter 600 concerning utility patent application contents	s.	ADDF	RESS	STO: Box	Patent A	ommissioner for Paten Application DC 20231	ıs . <u> </u>
1. Fee Transmittal Form (e.g., PTO/SB/17) (Submit an original and a duplicate for fee processing)						gram (Appendix)	
2. X Specification [Total Pages 26 (preferred arrangement set forth below)]] 6			and/or Amino e, <i>all neces</i> s		Sequence Submissio	n
- Descriptive title of the Invention		a.		Computer	Readab	ole Copy	
- Cross References to Related Applications		b.		Paper Cop	y (ident	tical to computer cop	y)
 Statement Regarding Fed sponsored R & D Reference to Microfiche Appendix 		c. [ĺ	Statement	verifyin	g identity of above o	opies
- Background of the Invention		L	<u> </u>			-	
- Brief Summary of the Invention	<u> </u>					LICATION PARTS	•
- Brief Description of the Drawings (if filed)	7					er sheet & document(s))
- Detailed Description	8			F.R.§3.73(b) there is an			
- Claim(s)	9	. E	Englis	h Translatio	n Docui	ment (if applicable)	
- Abstract of the Disclosure 3. X Drawing(s) (35 U.S.C. 113) [Total Sheets 2] 10			ation Disclo		Copies of Citations	IDS
4. Oath or Declaration [Total Pages] 11	. F	Prelim	inary Amend	dment		
a. Newly executed (original or copy)	12			Receipt Po			
Copy from a prior application (37 C.F.R. §	1.63(d))	ļ.,		ld be specific	-		
(for continuation/divisional with Box 16 complete	^(d) 13	. s	Stater	nent(s)		ement filed in prior ap us still proper and de	
i. DELETION OF INVENTOR(S) Signed statement attached deletir	ng [,,		PTO/S Certifi	6B/09-12)		Document(s)	Sirou
inventor(s) named in the prior applic	0(1.)	· <u> </u>	if fore	ign priority is	s claime	ed)	
see 37 C.F.R. §§ 1.63(d)(2) and 1.3		. X C	Other:	Certifica	ate of M	lailing	1111
FEES, A SMALL ENTITY STATEMENT IS REQUIRED (37 C.F.R. § 1.27), EX IF ONE FILED IN A PRIOR APPLICATION IS RELIED UPON (37 C.F.R. § 1.	(CEPT .28).						
16. If a CONTINUING APPLICATION, check appropriate box,		he reguisi	te info	rmation below	and in a	nreliminary amendmen	nt-
Continuation Divisional Continuation-in-				plication No: _		J.	
Prior application information: Examiner				Group / Art U	nit:		
For CONTINUATION or DIVISIONAL APPS only: The entire discleunder Box 4b, is considered a part of the disclosure of the acco	mpanying co	ontinuatio	on or o	divisional app	dication	and is hereby incorpo	rated by
reference. The incorporation can only be relied upon when a po				ly omitted fro	m the s	ubmitted application p	arts.
17. CO		ADDRI	<u>-SS</u>				
Customer Number or Bar Code Label (Insert Customer No.	883 or Attach bar	code labe	el here	or 🗀	Corre	espondence address be	'ow
PATENT TRAC	DEMARK OFFICE	***************************************		···			
Name							
Address							,
City Stat	e			Zip Co	ode		
Country Telephone					Fax		
Name (Print/Type) John Merchant	<u> </u>	D	o.ti *	Va (A": "		45 00G	$\overline{}$
		Hegistra	auon i	Vo. (Attorney/Ag		45,996	_
Signature of Weld	um X			1	Date	6-9-00	j

Burden Hour Statement: This form is estimated to take 0.2 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Box Patent Application, Washington, DC 20231.

+



PATENT TRADEMARK OFFICE

103.1032.02



Certificate of Mailing (37 C.F.R. § 1.10)

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Services on the date shown below as "Express Mail" (Post Office to Addressee) in an envelope addressed to the Commissioner of Patents and Trademarks, Washington, D.C. 20231.

Mailing Label No.	EL 524 781 248 US		
Date of Deposit:	6/09/2000		
Neerja Pathania		Neens	Pathonia
Printed Name		Signature	•

This application includes:

- Patent Application Form SB/05
- Specification 23 page(s)
- Claims 2 page(s)
- Abstract 1 page(s)
- Drawings 2 page(s)
- Certificate of Mailing
- Return Postcard

103.1032.02

PATENT TRADEHARK OFFICE

This application is submitted in the name of the following inventor(s): 1 2 <u>Inventor</u> Citizenship Residence City and State 3 English, Robert M. **United States** Menlo Park, California 5 The assignee is Network Appliance, Inc., a corporation having an office at 6 495 East Java Drive, Sunnyvale, California, 94089. 7 Title of the Invention Low-Overhead Threads in A High-Concurrency System Background of the Invention This application claims the benefit of U.S. Provisional Application No. 60/195,732, filed 4/7/00 (Attorney Docket number 103.1032.01). 16 1. Field of the Invention 17 18 This invention relates to low-overhead threads in a high-concurrency sys-19 tem, such as for a networked cache or file server. 20 21

2. Related Art

2

1

In many computing systems, it is desirable in certain circumstances to be 3 able to process, relatively simultaneously (such as in parallel), a relatively large number of similar tasks. For example, the same or similar tasks could be performed by a server 5 device (such as a file server) in response to requests by a number of client devices. One 6 such circumstance is in a networked cache or file server, which maintains and processes a 7 relatively large number of sequences of requests (sometimes called "connections"), so as to couple an information requester (such as a web client) to one or more information pro-9 10 11 12 1 viders, which are also coupled to the same internetworking system. One known method in which an individual processor or a multiprocessor system is able to maintain a high degree of concurrency is for the system to process each connection using a separate proc-13 14 essing thread. A "thread" is a locus of control within a process, indicating a spot within that process that the processor is then currently executing. In general, a thread has a rela-15 15 tively small amount of state information associated therewith, generally consisting only of a calling stack and a relatively small number of local variables. 16

17

18

19

20

21

22

High concurrency systems, such as networked caches and file servers used in an internetworking system, must generally maintain a large number of threads. Each information requester has its own separate connection for which the network cache or file server must maintain some amount of state information. Each such separate connection requires only a small amount of state information, such as approximately 100 to 200 bytes

of information. Since there are in many cases a relatively large number of individual connections, it would be desirable to be able to maintain state information about each such connection using only a relatively minimal amount of memory and processor overhead, while simultaneously maintaining both relatively reliable programmability and relatively high processing speed.

6

7

8

9

13 14 15

16

17

18

19

20

21

22

5

1

2

3

One problem with known systems is that allocation of state information for individual threads does not generally scale well. One of the problems with relatively large numbers of individual threads is that of allocating memory space for a calling stack for each one of those threads. In a first set of known systems, stack space for individual threads is allocated statically; this has the drawback that relatively large numbers of threads require a relatively large amount of memory to maintain all such stack spaces. Although the amount of stack space statically allocated for each individual thread can be reduced significantly, this has the drawback that operations that can be performed by each individual thread are similarly significantly restricted. In a second set of known systems, stack space for individual threads is allocated dynamically; this has the drawback that the minimum size for dynamic allocation of memory is generally measured in kilobytes, resulting in substantial unnecessary memory overhead. Although virtual memory can be used to store and retrieve stack space for individual threads in smaller increments, this has the drawback that compression and decompression of stack space for individual threads imposes substantial unnecessary processor overhead. In a third set of known systems, such as those using the Java programming language, dynamic memory allocation is used

to store and retrieve stack space for individual threads; this has the drawback that each procedure call within each thread imposes substantial unnecessary processor overhead.

3

4

5

7

8

9

16

17

18

19

20

21

22

An additional problem is introduced by the particular use made of multithreading by the WAFL file system (as described in the Incorporated Disclosures). In the WAFL file system, the C language "setimp" and "longimp" routines are combined with message passing among threads so as to support high concurrency using threads. In particular, the requester of an initial file request to the WAFL file system packages the request in a message, which the WAFL file system processes using ordinary procedural program code, so long as data is available for processing the request and the thread need not have its execution suspended. If the thread is suspended for any reason (such as if a resource is not available,) the WAFL file system: (1) requests the needed resource, (2) queues the message for signaling when the resource is available, and (3) calls the C routing "longjmp" to return to the origin of the routine for processing the message. Thus, the WAFL file system restarts processing the entire message from the very beginning until all needed resources are available and processing can complete without suspension. While this use of multithreading by the WAFL file system has the advantage that programmers do not need to encode program state when a routine is suspended, it has the disadvantage, when combined with multithreading, that all necessary data structures (to process any arbitrary message) must be collected before the entire message can be processed. In an internetworking environment, collecting all such structures can be difficult and subject to error.

2

and using relatively low-overhead threads in a high-concurrency system, such as for a 3 networked cache or file server, that is not subject to drawbacks of the known art. 4

5

6

7

8

9

17 18

19 20

22

21

Summary of the Invention

Accordingly, it would be advantageous to provide a technique for creating

The invention provides a method and system for providing the functionality of dynamically-allocated threads in a multithreaded system in which the operating system provides only statically-allocated threads. With this functionality, a relatively large number of threads can be maintained without a relatively large amount of overhead (either in memory or processor time), and it remains possible to produce program code without undue complexity.

In a preferred embodiment, a plurality of dynamically-allocated threads are simulated using a single statically-allocated thread, but with state information regarding each dynamically-allocated thread maintained within the single statically-allocated thread. The single statically-allocated thread includes, for each procedure call that would otherwise introduce a new dynamically-allocated thread, a memory block including: (1) a relatively small procedure call stack for the new dynamically-allocated thread, and (2) a relatively small collection of local variables and other state information for the new dynamically-allocated thread. When using multithreading in the WAFL file system, high concurrency among threads can be maintained without any particular requirement that the program code maintain a substantial amount of state information regarding each dynamically-allocated thread. Each routine in the WAFL file system that expects to be suspended or interrupted need maintain only a collection of entry points into which the routine is re-entered when the suspension or interruption is completed. A feature of the C language preprocessor allows the programmer to generate each of these entry points without substantial additional programming work, with the aid of one or more program-

8 ming macros.

The invention provides an enabling technology for a wide variety of applications for multithreaded systems so as to obtain substantial advantages and capabilities that are novel and non-obvious in view of the known art. Examples described below primarily relate to networked caches and file servers, but the invention is broadly applicable to many different types of automated software systems.

Brief Description of the Drawings

17

18

19

16

15

Figure 1 shows a block diagram of a system for providing functionality of low-overhead threads in a high-concurrency system, such as for a networked cache or file server.

21

Figure 2 shows a process flow diagram of a system for providing function-1 ality of low-overhead threads in a high-concurrency system, such as for a networked 2 cache or file server. 3

4

Detailed Description of the Preferred Embodiment

6

7

8

9

In the following description, a preferred embodiment of the invention is described with regard to preferred process steps and data structures. Embodiments of the invention can be implemented using general-purpose processors or special purpose processors operating under program control, or other circuits, adapted to particular process steps and data structures described herein. Implementation of the process steps and data structures described herein would not require undue experimentation or further invention.

Lexicography

17

16

The following terms refer or relate to aspects of the invention as described below. The descriptions of general meanings of these terms are not intended to be limiting, only illustrative.

19

22

18

20 21

client and server — In general, these terms refer to a relationship between two devices, particularly to their relationship as client and server, not necessarily to any particular physical devices.

7

15 16

17

18

19

20

21

22

EL 524 781 248 US

with a first server device, can serve as a server device in a second relationship with a second client device. In a preferred embodiment, there are generally a relatively small number of server devices servicing a relatively larger number of client devices.

For example, but without limitation, a particular client device in a first relationship

client device and server device — In general, these terms refer to devices taking

on the role of a client device or a server device in a client-server relationship (such

as an HTTP web client and web server). There is no particular requirement that

any client devices or server devices must be individual physical devices. They can

each be a single device, a set of cooperating devices, a portion of a device, or some

combination thereof.

For example, but without limitation, the client device and the server device in a

client-server relation can actually be the same physical device, with a first set of

software elements serving to perform client functions and a second set of software

elements serving to perform server functions

As noted above, these descriptions of general meanings of these terms are not intended to be limiting, only illustrative. Other and further applications of the inven-

tion, including extensions of these terms and concepts, would be clear to those of ordinary

skill in the art after perusing this application. These other and further applications are

2 part of the scope and spirit of the invention, and would be clear to those of ordinary skill

in the art, without further invention or undue experimentation.

System Elements

Figure 1 shows a block diagram of a system for providing functionality of low-overhead threads in a high-concurrency system, such as for a networked cache or file server.

A system 100 includes a networked cache or file server (or other device) 110, a sequence of input request messages 120, and a set of software elements 130.

The networked cache or file server (or other device) 110 includes a computer having a processor, program and data memory, mass storage, a presentation element, and an input element, and is coupled to a communication network. As used herein, the term "computer" is intended in its broadest sense, and includes any device having a programmable processor or otherwise falling within the generalized Turing machine paradigm. The mass storage can include any device for storing relatively large amounts of information, such as magnetic disks or tapes, optical devices, magneto-optical devices, or other types of mass storage.

1 networked cache or file server 110 to perform actions in response thereto. In a preferred 2 embodiment, the actions to be performed by the networked cache or file server 110 will 3 involve access to the mass storage or to the communication network. In a preferred embodiment, the input request messages 120 are formatted in a known request protocol, such 5 as NFS, CIFS, HTTP (or variants thereof), but there is no particular requirement for the input request messages 120 to use these known request protocols or any other known re-7 quest protocols. In a preferred embodiment, the networked cache or file server 110 re-8 sponds to the input request messages 120 with both: (1) a condign set of responsive ac-9 tions involving the mass storage or the vacation network, and (2) a condign response to the input request messages 120, the response to the input request messages 120 preferably taking the form of a set of response messages (not shown.)

16

17

18

The software elements 130 include a set of programmed routines to be performed by the networked cache or file server 110, using the functionality of low-overhead threads and high-concurrency as described herein. Although particular program code is described herein with regard to the programmed routines, there is no particular reason that the software elements 130 must use the specific program code described herein, or any other specific program code.

The input request messages 120 include a set of messages requesting the

20

Method of Operation

Figure 2 shows a process flow diagram of a system for providing functionality of low-overhead threads in a high-concurrency system, such as for a networked cache or file server.

A method 200 includes a set of flow points and a set of steps. The system 100 performs the method 200. Although the method 200 is described serially, the steps of the method 200 can be performed by separate elements in conjunction or in parallel, whether asynchronously, in a pipelined manner, or otherwise. There is no particular requirement that the method 200 be performed in the same order in which this description lists the steps, except where so indicated.

At a flow point 210, the networked cache or file server 110 is ready to receive and respond to the input request messages 120.

At a step 211, the networked cache or file server 110 receives an input request message 120, and forwards that input request message 120 to an appropriate software element 130 for processing. In a preferred embodiment, the step 211 includes performing a calling sequence for the software element 130, including possibly creating a simulated dynamically allocated thread (that is, a thread simulated so as to appear to be dynamically-allocated, hereinafter sometimes called a "simulated thread" or an "S-

thread") within which the software element 130 is performed. Thus, the software element

2 130 can be created using program code that assumes that the software element 130 is per-

formed by a separate thread and does not demand relatively excessive resources (either

4 memory or processor time.)

5

6

7

3

As part of step 211, the networked cache or file server 110 allocates a procedure call block 131 and a local variable block 132, for use by the simulated dynamically-allocated thread performed by the software element 130. The procedure call block 131 includes a set of input variables for input to the software element 130, a set of output variables for output from the software element 130, and such other stack element as is known in the art of calling stacks for procedure calls. The local variable block 132 includes a set of locations in which to store local variables for the software element 130.

16

17

18

19

20

As part of step 211, the networked cache or file server 110 determines whether the software element 130 is a subroutine of a previously called software element 130 in the same simulated thread. If so, the networked cache or file server 110 indicates that fact in a block header 133 for the software element 130, so as to point back to the particular software element 130 that was the parent (calling) software element 130. If not, the networked cache or file server 110 does not indicate that fact in the block call or block header for the software element 130.

As part of this step, the networked cache or file server 110 determines 1 whether the software element 130 is to be performed by a new simulated thread. If so, the 2 networked cache or file server 110 adds the new thread block 134 to a linked list 135 of 3 thread blocks 134 to be performed in turn according to a scheduler. In a preferred em-4 bodiment, the scheduler simply performs each simulated thread corresponding to the next 5 thread block 134 in round-robin sequence, so that each simulated thread corresponding to a thread block 134 is performed in its turn, until it is suspended or completes. However, 7 in alternative embodiments, the scheduler may select simulated threads in other than a 8 round-robin sequence, so as to achieve a desired measure of quality of service, or other 12 12 administrative goals.

At a step 212, the networked cache or file server 110 chooses the simulated thread for execution. The simulated thread, with appropriate data completed for the procedure call block 131 and local variable block 132, is performed in its turn, until it is suspended or completes. If the simulated thread is capable of completing its operation without being suspended or interrupted, the scheduler selects the next thread block 134 in the linked list of thread blocks 134 to be performed in turn.

18

19

20

21

13

15

16

17

After this step, the method 200 has performed one round of receiving and responding to input request messages 120, and is ready to perform another such round so as to continuously receive and respond to input request messages 120.

The method 200 is performed one or more times starting from the flow point 210 and continuing therefrom. In a preferred embodiment, the networked cache or file server 110 repeatedly performs the method 200, starting from the flow point 210 and continuing therefrom, so as to receive and respond to input request messages 120 periodically and continuously.

Program Structures

7

8

A set of program structures in a system for providing functionality of lowoverhead threads in a high-concurrency system, such as for a networked cache or file server, includes one or more of, or some combination of, the following:

12 13 14

• A set of program structures for declaring and creating a dynamically-allocated thread in a system in which threads are usually statically-allocated;

16

17

typedef struct {

18

19 } function_msg;

// local variables

int arg;

20

21

22

In the program structure above, the definition for the structure type "function_msg" includes: (1) the local variables for the dynamically-allocated thread, (2) any

// an example, not necessary

- input arguments to the dynamically-allocated thread, in this case just the one variable
- 2 "arg", and (3) any output arguments from the dynamically-allocated thread, in this case
- 3 none.

A set of program structures for denoting program code entry-points for a simulated
 thread;

```
static void
              function sthread(sthread msg *m)
              {
                  function_msg * const msg = m->data;
STHREAD START BLOCK (m);
                  // executable C code
                  STHREAD RESTART POINT (m); // an example
         blocking point
                  // executable C code
                  STHREAD_COND_WAIT (m, cond (m)); // encapsulated
         blocking point
                  // executable C code
                  STHREAD_END_BLOCK;
20
                  free (msg);
21
             }
22
```

The program structure above includes, in its definition for the function "function_sthread", an initial program statement obtaining access to the local variables for the simulated thread. This is the statement referring to "m -> data".

2

simulated thread. This is the statement "STHREAD START BLOCK (m)", which 3

makes use of a macro defined for the name "STHREAD START BLOCK". 4

5

6 .

8

13

16

17

18

19

20

21

The program structure above includes a definition for a restart-point for the

simulated thread. This is the statement "STHREAD RESTART POINT (m)", which makes use of a macro defined for the name "STHREAD RESTART POINT".

The program structure above includes a definition for a start-point for the

The program structure above includes a definition for a conditional-wait point (a possible suspension of the simulated thread) for the simulated thread. This is the statement "STHREAD COND WAIT(m, cond(m))", which makes use of a macro defined for the name "STHREAD COND WAIT".

The program structure above includes, in its definition for the function "function sthread", a closing program statement for ending the simulated thread. This is the statement "STHREAD END BLOCK", which makes use of a macro defined for the name "STHREAD END BLOCK". The program structure above also includes a statement for freeing any data structures used by the simulated thread. This is the statement "free(msg)".

The macro definitions for "STHREAD_START_BLOCK",

"STHREAD_RESTART_POINT", and "STHREAD_END_BLOCK" collectively form

a C language "case" statement.

The macro "STHREAD_START_BLOCK" includes the preamble to the

"case" statement:

#define STHREAD_START_BLOCK (m) switch (m -> line) { case 0:

• The macro "STHREAD_RESTART_POINT" includes an intermediate restart point in the "case" statement:

```
#define STHREAD_RESTART_POINT(m) case __LINE__: m -> line = __LINE__
```

The restart point uses the C preprocessor to generate tags that the switch statement uses as branch points. The C macro __LINE__ substitutes the line number of the file being processed, so a series of restart points generates a series of unique cases within the switch. Setting m -> line to the case just entered means that if the procedure is re-entered the switch statement will branch to the restart point and continue.

21

8

13

17

18

19

• The macro "STHREAD_START_BLOCK" includes the close of the "case" statement:

```
#define STHREAD_END_BLOCK }
```

Thus, the C preprocessor generates a "case" statement in response to use of these macros, which allows the programmer to easily specify each of the proper restart points of the routine.

• A set of program structures for suspending and restarting simulated threads;

```
#define STHREAD_COND_WAIT(m, c) \
STHREAD_RESTART_POINT(m); \
    {\ if (c) \
        sthread_suspend(); \
}
```

[]

At an individual restart point, the programmer can use the macro "STHREAD_COND_WAIT" to conditionally either wait for an operation to complete, or to suspend and restart the simulated thread while waiting for resources for the operation to complete.

• A set of program structures for initiating simulated threads;

The macro "STHREAD_INIT" allocates memory for the simulated thread, sets the

5

```
7
```

15 W I 17

22

23

24

C preprocessor value __LINE__ to zero, sets the value of "data" to the private stack area of the particular simulated thread, and sets a value for "handler" to a function passed to the macro as an argument.

```
#define STHREAD INIT(m, msg, handler) \ m = malloc(sizeof(*m)); \
msg = zalloc(sizeof(*msg)); \ m -> line = 0; \ m -> data = msg; \ m ->
handler = handler
```

A set of program structures for actually performing the simulated thread;

```
void
    function(int arg)
    {
         function_msg *msg;
         sthread msg *m;
         STHREAD_INIT(m, msg, function_sthread);
         msg->arg = arg;
         sthread_run(m);
    }
```

The program structure above includes, in its definition for the function "function", program code for creating the data blocks for the simulated thread, and for placing data in those data blocks. These are the statements "STHREAD_INIT(m, msg, function_sthread)" and "msg -> arg = arg", which make use of a macro defined for the name "STHREAD_INIT".

6

7

• A set of program structures for scheduling performance of simulated threads;

8

```
switch (m->line) {  // a field in sthread_msg
case 0:
  // executable C code
STHREAD_RESTART_POINT(m);
  // executable C code
STHREAD_RESTART_POINT(m);
  // executable C code
}
```

18

20

21

22

The program structure above includes, in its definition for the function "function", program code for creating the data blocks for the simulated thread, and for placing data in those data blocks. These are the statements "STHREAD_INIT(m, msg, function_sthread)" and "msg -> arg = arg", which make use of a macro defined for the name "STHREAD INIT".

 A set of program structures for suspending and resuming performance of simulated threads.

```
3
```

2

```
typedef struct sthread_msg {
 4
                     int line;
 5
                     void *data;
                     void (*handler)(sthread_msg *);
 8
           jmp_buf sthread_env;
 9
                void
10
                sthread run(sthread_msg *m)
11
{
                     if (!setjmp(sthread_env)) {
                           m->handler(m);
                           free(m);
                      }
                }
           void
                sthread_suspend()
                {
                      longjmp(sthread_env, 0);
21
                }
22
           sthread_msg *suspended_sthread;
23
                int ready;
24
                int
25
                cond(sthread_msg *m)
26
27
                      if (ready)
28
```

21

and

```
return 1;
    suspended_sthread = m;
    sthread_suspend();
}
int
set_cond()
{
    ready = 1;
    if (suspended_sthread) {
         sthread msg *m = suspended_sthread;
         suspended_sthread = 0;
         sthread_run(m);
    }
// cond() changed
sthread run(suspended_sthread);
```

- A set of program structures for performing simulated threads in conjunction with the WAFL file system, as shown above.
- 23

Generality of the Invention

2

1

The invention has general applicability to various fields of use, not necessarily related to the services described above. For example, these fields of use can include devices other than file servers.

6

7

Other and further applications of the invention in its most general form, will be clear to those skilled in the art after perusal of this application, and are within the scope and spirit of the invention.

Technical Appendix

The technical appendix enclosed with this application is hereby incorporated by reference as if fully set forth herein, and forms a part of the disclosure of the invention and its preferred embodiments.

16

17

13

15

Alternative Embodiments

18

19

20

21

Although preferred embodiments are disclosed herein, many variations are possible which remain within the concept, scope, and spirit of the invention, and these variations would become clear to those skilled in the art after perusal of this application.

1	Claims
2	
3	1. A method including
4	simulating a plurality of dynamically-allocated threads using a statically-
5	allocated thread; and
6	maintaining state information regarding each dynamically-allocated thread
7	maintained within said statically-allocated thread.
8	
9	2. A method as in claim 1, including maintaining, for a routine capable
10	of being suspended or interrupted, a set of entry points into which said routine is capable
4	of being re-entered after said suspension or interruption.
	3. A method as in claim 1, including generating said set of entry points
14	in response to one or more programming macros.
13	
16	4. A method as in claim 1, including maintaining high concurrency
17	among threads without maintaining a substantial amount of state information regarding
18	simulated threads.
19	
20	5. A method as in claim 1, wherein said state information includes a
21	relatively small procedure call stack for the simulated thread.

EL 524 781 248 US

1	6. A method as in claim 1, wherein said state information includes a
2	relatively small collection of local variables and other state information for the simulated
3	thread.

7. Apparatus including a file server system having a statically-allocated thread including a plurality of simulated dynamically-allocated threads, said statically-allocated thread including state information regarding each said simulated thread.

8. Apparatus as in claim 7, including a routine capable of being suspended or interrupted, said routing having a set of entry points into which said routine is capable of being re-entered after said suspension or interruption.

9. Apparatus as in claim 8, wherein said set of entry points are responsive to one or more programming macros.

10. Apparatus as in claim 7, wherein said state information includes a relatively small procedure call stack for the simulated thread.

11. Apparatus as in claim 7, wherein said state information includes a relatively small collection of local variables and other state information for the simulated thread.

Abstract of the Disclosure

2

3

4

5

6

7

8

45

16

17

18

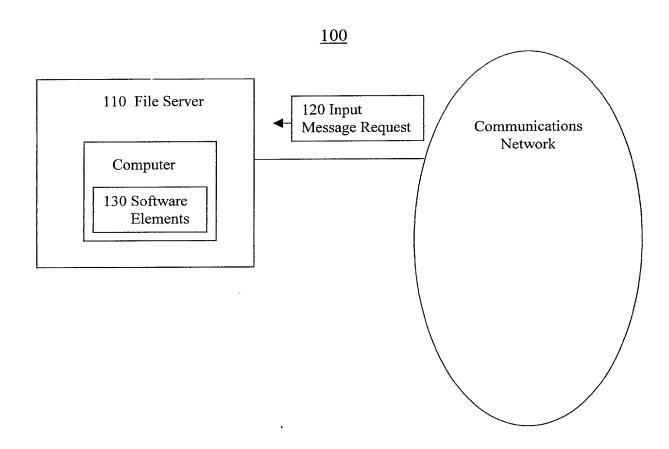
19

20

21

1

The invention provides a method and system for providing the functionality of dynamically-allocated threads in a multithreaded system, in which the operating system provides only statically-allocated threads. With this functionality, a relatively large number of threads can be maintained without a relatively large amount of overhead (either in memory or processor time,) and it remains possible to produce program code without undue complexity. A plurality of dynamically-allocated threads are simulated using a single statically-allocated thread, but with state information regarding each dynamicallyallocated thread maintained within the single statically-allocated thread. The single statically-allocated thread includes, for each procedure call that would otherwise introduce a new simulated thread, a memory block including (1) a relatively small procedure call stack for the new simulated thread, and (2) a relatively small collection of local variables and other state information for the new simulated thread. When using multithreading in the WAFL file system, high concurrency among threads can be maintained without any particular requirement that the program code maintain a substantial amount of state information regarding each dynamically-allocated thread. Each routine in the WAFL file system that expects to be suspended or interrupted need maintain only a collection of entry points into which the routine is re-entered when the suspension or interruption is completed. A feature of the C language preprocessor allows the programmer to generate each of these entry points without substantial additional programming work, with the aid of one or more programming macros.



2/2

Fig.2

